Multi-Frame Blind Deconvolution Accelerated with Graphical Processing Units

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ABSTRACT

Ground-based observations of Low Earth Orbit (LEO) satellites are significantly degraded by atmospheric turbulence. Multi-Frame Blind Deconvolution (MFBD) is a class of algorithm that attempts to solve this ill-posed inverse problem using a forward model approach. It assumes that a Point Spread Function (PSF) is convolved with a pristine image of the target and then iteratively solves for both simultaneously. Processing one full LEO collection in this way requires an extraordinary amount of processing power and is a task that is normally performed with supercomputers to minimize processing time. In this paper, we describe a new MFBD implementation written with NVidia’s Compute Unified Device Architecture (CUDA) language to make use of the incredible parallelization capabilities of graphical processing units (GPUs). This reduces the scope of hardware capable of achieving the same processing time from a supercomputer-scale system to a single desktop or server. Measurements of processing time and image quality for several reference objects are compared against results from the latest version of Physically Constrained Iterative Deconvolution (PCID), a gold-standard MFBD implementation that uses the Message Passing Interface (MPI) to make use of numerous CPUs in high-performance computing environments.

1. INTRODUCTION

The Air Force Maui Optical and Supercomputing (AMOS) site hosts a 1.6-meter and a 3.6-meter telescope on the summit of Mt. Haleakala. At an observing wavelength of 800 nm, the diffraction-limited resolution of the 3.6-m telescope is:

\[
\frac{\lambda}{D} = 0.22 \text{ \mu rad}
\]  

(1)

Observing a Low Earth Orbit (LEO) object at a distance of 500 km, and in perfect conditions, this telescope should be able to resolve details as small as 11 cm. However, even Haleakala’s exceptionally still atmosphere imposes significant blurring due to turbulence; with a median Fried’s parameter (\(r_0\)) value of 10 cm \[1\], the effective resolution is only:

\[
\frac{\lambda}{r_0} = 8.0 \text{ \mu rad}
\]  

(2)

This limits the spatial resolution to 4 meters while observing an object 500 km away. The random nature of turbulence may permit using lucky imaging to sometimes resolve fine details, but Fried \[2\] suggests that the probability of getting a good image for a 3.6-m diameter aperture at \(r_0 = 10\) cm is:

\[
5.6e^{0.1557(D/r_0)^2} = 10^{-87}
\]  

(3)

For comparison, the chance of being struck by lightning in the United States at least once in a human lifetime is about \(10^{-6}\), and the odds of winning some of the largest lotteries is about \(10^{-9}\).

Thankfully, numerous techniques have been developed to mitigate the blurring effects of turbulence, including hardware-based solutions such as Adaptive Optics (AO) \[3, 4, 5, 6\] as well as numerous software-based image recovery algorithms such as bispectrum image recovery \[7, 8, 9\] and Multi-Frame Blind Deconvolution (MFBD) \[10, 11, 12, 13\],...
among others. The gold-standard image recovery algorithm used at the AMOS site is Physically-Constrained Iterative Deconvolution (PCID) [14, 15, 16, 17], an implementation of MFBD that attempts to simultaneously recover the object and the blur function using iterative maximum likelihood estimation while imposing a number of physical constraints, such as positivity and support constraints. PCID (like most MFBD algorithms) is computationally expensive, relying primarily on Fast Fourier Transforms (FFTs), and producing timely output images necessitates using high-performance computing (HPC) resources dedicated to this task.

Researchers and HPC systems alike have been shifting toward using commonly-available General-Purpose Graphical Processing Unit (GPGPU) hardware for solving problems with a high degree of data parallelization [18] using software architectures such as Open Computing Language (OpenCL) [19] and NVidia’s Compute-Unified Device Architecture (CUDA) [20, 21]. It’s easy to imagine how hardware designed to operate on image data could be more efficient at running image recovery algorithms than a more generic Central Processing Unit (CPU); any given image recovery technique is likely going to perform a common set of operations on an array of pixel data, or multiple arrays of such data, which is almost an ideal use case for GPGPUs. PCID in particular uses numerous Fast Fourier Transforms (FFTs) during the iterative minimization process, and for normal usage its overall performance is constrained by the rate at which it can perform 2D FFTs. However, PCID only makes use of Central Processing Unit (CPU) hardware with Message Passing Interface (MPI) [22] for parallelization between multiple CPUs. It uses the very well-written Fastest Fourier Transform in the West (FFTW) [23], but even images as small as 64x64 can be processed more than an order of magnitude faster using the CUDA API’s cufft library [24, 25].

In this paper we describe a new MFBD algorithm that is implemented in CUDA. The MFBD algorithm, named Likelihood-based Uncertainty-Constrained Iterative Deconvolution (LUCID), will be described in Section 2. CUDA-specific implementation details are described in Section 3. Sections 4 describes the performance testing configuration and Section 5 describes the performance measurements for the LUCID and PCID algorithms. This paper will conclude with a summary of the results and potential improvements in Section 6.

2. LUCID

The LUCID algorithm is fundamentally based on the PCID algorithm. It assumes a simple linear forward model for imaging through turbulence. Each $m$th measurement of short-exposure image data, $d_m(x)$, is represented by a pristine image of the object, $o(x)$, convolved with a Point Spread Function (PSF), $h_m(x)$, plus some additive noise $n_m(x)$, as shown in the Equation 4:

$$d_m(x) = o(x) * h_m(x) + n_m(x)$$  \hspace{1cm} (4)

Equation 4 assumes that the pristine object $o(x)$ is unchanging across all $m$ measurement frames. As a practical concern, this equation remains approximately true for only a few seconds at a time when observing a LEO satellite from a fixed ground site due to naturally-occurring changes in the target’s pose. If the satellite is spinning or possess moving components, then the length of time for which this equation remains approximately true is further constrained. Thus, processing a full pass entails grouping collected images into a series of short ensembles that are processed separately. For each of these image ensembles, the object and PSFs are estimated simultaneously in the image domain by minimizing the following cost function:

$$J[o(x), h_1(x), ..., h_m(x)] = \sum_{m=1}^{M} \sum_{n=1}^{N} \frac{(i_m(x_n) - d_m(x_n))^2}{\sigma^2(x_n) + l_m(x_n)}$$  \hspace{1cm} (5)

Equation 5 is the sum over $M$ frames and $N$ pixels per frame of the noise-normalized squared residual between the measured data frames $d(x_n)$ and the data model $i(x_n)$. The denominator contains read noise variance per-pixel of $\sigma^2(x_n)$ and shot noise variance of $l_m(x_n)$. Each $m$th model of the data is calculated from the convolution of the estimated object frame with each estimated PSF:

$$i_m(x_n) = o(x) * h_m(x_n)$$  \hspace{1cm} (6)

New object and PSF images are estimated using the Limited-memory Broyden-Fletcher-Goldfarb-Shanno (L-BFGS)
quasi-Newton method [26, 27] with the cost function defined in Equation 5. The data model frames are regularized using the filter-based regularization scheme used by the PCID algorithm [16], wherein \( \text{im}(x_n) \) is convolved with a 2D filter so as to achieve a reduction in noise amplification. However, noise-amplification is still inescapable, so output images are saved as a function of iteration number. The best image in each ensemble is chosen later from an image quality assessment performed on the full range of the ensemble’s output images.

3. CUDA

Using CUDA to implement a MFBD algorithm introduces a number of specific concerns that do not exist in a CPU implementation. First, it is generally understood that GPU processing suffers from significant overhead in moving data to and from the GPU device [21]. Thus, the software moves a full ensemble of image data to the device once and then perform all subsequent processing on the GPU. Figure 1 demonstrates this; the partitioning of a collection into short ensembles is performed by the CPU, and then all of the data is sent to and remains on the GPU until processing is complete.

Fig. 1: Model of how data is transferred between host (CPU) and device (GPU) memory during LUCID execution

Consumer-grade GPUs, such as NVidia’s GTX and RTX series, are optimized for single-precision (float32) computation; while they are still capable of performing double-precision (float64) calculations, the performance on these devices suffers by 1/32, as shown in the Arithmetic Instructions section of the CUDA Programming Guide [21]. For CPUs, and for NVidia’s datacenter-grade Tesla line, the performance scaling is normally 1/2. The additional precision available to float64 has no impact on LUCID’s (or PCID’s) ability to reconstruct images during the minimization process, which leads to a design that uses 32-bit precision wherever possible.

4. PERFORMANCE TEST SETUP

Images of Seasat and Hubble Space Telescope (HST) were collected with a EMCCD camera on the 3.6-meter Advanced Electro-Optical System (AEOS) Telescope. All images were collected with an exposure time of 15 ms and in high-gain EMCCD mode for a collection of at least 10,000 frames (75 seconds) per target. Examples of these images are shown in Figure 2.

For a collection with a fixed number of ensembles, the total processing time is primarily dependent on the size of each ensemble and the number of processing iterations. Thus, these are the parameters varied for a processing time comparison between LUCID and PCID. These algorithms both use the same cost function (Equation 5) and the same minimizer (L-BFGS [26, 27]), but PCID runs on CPU cores whereas LUCID uses a GPU. Each satellite collection is reduced to a 10,000 frame dataset and then split up into 150 separate ensembles, where the start point of each ensemble is separated by 1 second (67 frames). Total processing time is measured as a function of ensemble size, which is varied between 10 and 300 frames while performing 50 MFBD iterations per ensemble, and as a function of number of MFBD iterations per ensemble, which is varied between 10 and 300 with an ensemble size of 50 frames.
Processing time is measured on a handful of hardware platforms. The PCID algorithm executed on a system with a Intel Xeon E5-2698v4 20-core Broadwell CPU using all available cores. LUCID was executed on two different GPUs in separate benchmarking experiments: an NVidia GTX 980 and an NVidia Tesla V100. Each benchmarking experiment is granted exclusive use of the hardware so as to minimize uncertainty. Benchmarking measurements are made using Python’s `timeit` module to measure the full execution time of the process wrapped in a `subprocess.Popen` instance. This includes the time to read image data from disk, write data to disk, and (in the case of LUCID only) transfer data from the host to the GPU device. Each experiment is executed 10 times, and then 95% confidence intervals are estimated.

5. PERFORMANCE MEASUREMENTS

The LUCID and PCID algorithms produce images of equivalent quality. Examples of LUCID output images are shown in Figure 3, but both algorithms produced images with the same image quality.
Figure 4 shows the average processing time per ensemble as a function of iteration number and Figure 5 shows the average processing time per ensemble as a function of ensemble size for each of the satellites considered in this paper. Each data point in these figures reports the mean and standard deviation of 10 benchmark experiments. Figure 6 shows the ratio of LUCID processing time to PCID processing time for each of the objects and varied parameters.

6. CONCLUSIONS AND FUTURE WORK

LUCID is an application written in C++ and CUDA that uses GPU hardware to quickly process degraded images of LEO satellites using a MFBD implementation. Figure 6 shows that LUCID running on a single Tesla V100 with 100 MFBD iterations can process data up to 10 times faster than PCID using all 20 cores of a recent-generation CPU. The shapes of these curves demonstrate the presence of overhead cost when using the GPU for very small problem sizes (10 iterations or 10 frames per ensemble), and that this performance cost becomes negligible for larger problem sizes. However, LUCID still runs 4 times faster even in the worst case on the V100. This suggests that LUCID has supercomputer-equivalent capabilities to PCID, especially on systems that may have multiple V100 GPUs.
Fig. 6: 95% confidence intervals for the ratio of PCID (CPU) processing time per ensemble to LUCID (GPU) processing time per ensemble when varying (a) the number of iterations and (b) the number of frames per ensemble. A grey line is drawn at a ratio of 1 for visual reference, as any data points greater than 1 illustrate GPU performance exceeding CPU performance.

per computational node, representing a significant reduction in hardware procurement and sustainment costs. The difference in performance ratio between Seasat and HST data shown in Figure 6 is well-explained by Seasat data being present in about 10% more of the camera pixels on average than the HST data, by virtue of some combination of differences in object size and PSF size.

The performance improvement when running LUCID with a GTX 980 is a little less promising, offering about 2 times more speed than PCID. However, the economics of this comparison paint a considerably brighter picture. First, the GTX 980 is considerably older hardware than the Intel Xeon E5-2698v4 against which it is being compared; the GTX 980 GPU was released in 2014 [28] whereas the Xeon used in this study was released in 2016 [29]. The GPU was also about 6 times cheaper on its launch date than the CPU was on its launch date according to each manufacturer’s suggested retail price (MSRP). With that in mind, future LUCID benchmarking measurements should include recently released consumer-grade GPU hardware, such as the GTX 2080. The advantage of the GPU acting as a coprocessor should also be mentioned; LUCID’s performance does not suffer when other CPU-using applications are being used on the same system.

Future efforts will include refinement of the algorithm to further improve processing time as well as image quality. The current prototype has numerous avenues for potential improvements that should reduce the processing time, improve the image quality, or both. For instance, much of the LUCID source code reuses device memory from already-processed ensembles so as to reduce superfluous (and expensive) memory allocation, but the L-BFGS implementation reallocates memory for each new ensemble; the processing time of the algorithm could be improved substantially by providing static memory allocations to L-BFGS for subsequent ensembles. The implementation also uses a single CUDA stream, but multiple streams could be used so as to hide some of the overhead associated with moving data to and from the device [21]. There may be additional avenues for processing time improvements that can be unveiled during future profiling efforts. We are also planning to implement an iteratively reweighted MFBD feature that may provide numerous benefits to image quality [30].

7. REFERENCES


[29] Intel xeon processor e5-2698 v4 (50m cache, 2.20 ghz) product specifications, 2016.